

1 ABSTRACT

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3 A system and method is provided for creating a digital circuit, such as a configured
4 FPGA or a VLSI chip. User code includes algorithm specifications having precisely defined
5 operators and variables, data representation specifications and data communication
6 specifications. The user code is compiled to create a digital circuit. For example, the compiled
7 user code is used to generate either a hard-wired digital circuit or the configuration instructions
8 for configuring a re-configurable digital circuit.

1005103.01202